



**NAME OF PERSON** Kevin Chivers

**NATIONALITY** British

**POSITION** Signalling Principles Design Engineer and Verifier

**QUALIFICATIONS** IRSE Signalling Principles Designer 1.1.160v3  
IRSE Signalling Design Verifier 1.1.550v3  
Engineering Technician (EngTech)  
Associate Member of the Institution of Railway Signal Engineers (AMIRSE)  
Member of the Institution of Engineering & Technology (TMIET)  
Member of Signalling Circuits Working Group (SCSWG)  
NCFE (A1) IRSE Workplace Assessor (under mentorship)  
HND (Electrical and Electronic Engineering) 2009

**TRAINING** SORAT Assessor – RSK Trained  
Route Relay Interlocking (RRI) DO 850 – Trained  
Intermediate Signalling Technology (IST) – Completed  
SWT Mod5 – Trained

**KEY EXPERIENCE** A professional railway signalling engineer with 11 years experience.

Kevin is multi-skilled engineer in both Mainline and Mass Transit Signalling disciplines. He has experience of all aspects of Signalling design from early GRIP stage feasibility work, to scheme development right the way through to GRIP Stages 5 – 8 on Mainline Network Rail infrastructure applications.

Kevin also has extensive experience of traditional London Underground Signalling Principles and modern CBTC/TBTC computerised rapid transit applications.

## EXPERIENCE

**August 2017 to date** **Signalling Principles Designer & Verifier, Kilborn Consulting Limited**

### Work undertaken during this period:

Development of a Feasibility Study for Feltham Depot including application of SsPAM charts and consideration of the interfaces with the Feltham Resignalling Project.

**June 2016 to June 2017** **Signalling Principles Designer & Verifier, Linbrooke Design Services**

### Work undertaken during this period:

Worked on the Laurencekirk Electro-Mechanical Signal Box life extension project - my responsibilities were the complete Signal Box design with bespoke circuitry and all outlying Signalling works.

E810 report and Signalling Design Specification for Kilkerran Electro-Mechanical Signal Box life extension project.

E810 report on Maghull MCB-CCTV level crossing.

EMC Report for Yagi Antenna

Waterloo Location Design including use of EBI400 track circuits.

**June 2016 to June 2017** **Signalling Principles Designer & Verifier, Amey**

### Work undertaken during this period:

My responsibilities were independently verifying Scheme Plans that were implementing Impedance Bonds for the new Auto-Transformer sections on the Crossrail project.

**June 2014 to March 2016      Signalling Principles Designer & Verifier, Network Rail**

**Work undertaken during this period:**

Worked on Scheme Development for the large and complex Cambridge area and outlying Interlockings. Also upgrades to many outlying Level Crossings to MCB-OD functionality. Production of E810 report for all Interlockings within the area, design and development of multiple scheme plans and providing intuitive solutions to a wide-ranging set of problems.

Multiple presentations to Major Scheme Review Panel (MSRP).

**August 2013 to July 2014      Signalling Designer, Siemens Transportation**

**Work undertaken during this period:**

Worked on the Berks and Hants Re-lock and Immunisation Project.

My responsibilities were the design of the Hungerford Fringe, and the re-control and interfacing of Ufton Nervet Level Crossings designs with WESTLOCK Interlockings.

**January 2012 to July 2013      Signalling Designer; URS (Aecom)**

**Work undertaken during this period:**

Rochdale and Castleton East Junction Re-Signalling. Upgrading and life extending the Electro-Mechanical Signal Box and altering the method of working from Absolute Block to Track Circuit Block.

My responsibilities were complete design of Signal Box to bespoke circuitry and all outlying signalling designs including locations, bonding, aspect sequence charts etc.

**October 2006 to January 2012      Trainee / Assistant Designer; Invensys Rail (Westinghouse Rail Systems)**

**Work undertaken during this period:**

Working on the Victoria Line Upgrade (VLU) Mass Transit Project for London Underground. My responsibilities were wide ranging from Control Table design, production of DTG-R scale plans, designing application data and WESTRACE data.

I also designed screen layouts for the MCT.

<b>Languages</b>	<b>Spoken</b>	<b>Written</b>	<b>Reading</b>
English (Mother Tongue)	Yes	Yes	Yes